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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/649,827	08/29/2000	Edward A. Schrock	303.527US2	8668

21186 7590 07/08/2002

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EXAMINER

GALLAGHER, JOHN J

ART UNIT	PAPER NUMBER
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1733

DATE MAILED: 07/08/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

AS-11

# Advisory Action

Application No.  
09/649,827

Applicant(s)  
SCHROCK et al

Examiner  
John GALLAGHER

Art Unit  
1733



-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED Jun 14, 2002 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid the abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

THE PERIOD FOR REPLY [check only a) or b)]

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
- b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☒ The proposed amendment(s) will not be entered because:
- (a) ☒ they raise new issues that would require further consideration and/or search (see NOTE below);
- (b) ☐ they raise the issue of new matter (see NOTE below);
- (c) ☒ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
- (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: the methods now newly proposed for claims 39 and 44 are completely different from (ie directed to a different embodiment than) those previously presented; further along this line, N.B. MPEP 714.13, second SECTION

3. ☐ Applicant's reply has overcome the following rejection(s): \_\_\_\_\_
4. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for reconsideration has been considered but does NOT place the application in condition for allowance because: \_\_\_\_\_
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☒ will not be entered or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
- The status of the claim(s) is (or will be) as follows:
- Claim(s) allowed: None
- Claim(s) objected to: None
- Claim(s) rejected: 34-63
- Claim(s) withdrawn from consideration: \_\_\_\_\_
8. ☐ The proposed drawing correction filed on \_\_\_\_\_ is a) ☐ approved or b) ☐ disapproved by the Examiner.
9. ☒ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). 2
10. ☒ Other: See page 2 of this action.

Art Unit: 1733

It is reiterated (N.B. paragraph 8 of the Final Rejection) that the “electrical” and “electronic” (e.g. “chip”) components employed by the respective patentees KING (and NOT CHING as mistakenly set forth) and BRADLEY encompass within their scope and definition semiconductor dice (the elements envisioned for use by applicants); further, the foregoing (ie parenthesized) terminology would fairly and clearly suggest this to those of ordinary skill in this art; still further regarding (and in support of) the foregoing, N.B. col. 1 lines 13-16 of the TSUKAGOSHI et al reference (copy attached as a part of this action), cited in accordance with the instructions and provisions of MPEP 2144.03.

  
J.J. Gallagher:jig

05 July 2002

JOHN J. GALLAGHER  
PRIMARY EXAMINER  
ART UNIT 1733